



Docket No.: M4065.0363/P363-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Design Application of:
Mark E. Tuttle

Allowed: March 22, 2005

Application No.: 10/003,116

Confirmation No.: 5771

Filed: December 6, 2001

Art Unit: 2813

For: METHOD FOR FORMING A
STRUCTURE FOR SUPPORTING AN
INTEGRATED CIRCUIT CHIP

Examiner: N. Berezny

**COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

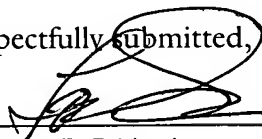
MS Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant agrees in part with the Examiner's reasons for allowance in that the prior art of record fails to disclose the limitations noted by the Examiner. However, the Examiner's statement does not reflect all of the language of each of the allowed claims. The totality of the language of each claim provides additional reasons for the allowance of each claim.

Dated: June 21, 2005

Respectfully submitted,

By 
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In re Patent Application of:
Mark E. Tuttle

Examiner: Nema O. Berezny

Application No.: 10/003,116

Art Unit: 2813

Filed: December 6, 2001

For: METHOD OF FORMING A STRUCTURE
FOR SUPPORTING AN INTEGRATED
CIRCUIT CHIP

SUBMISSION OF REPLACEMENT SHEET OF DRAWINGS (FIGS. 1-2)

Commissioner for Patents
Alexandria, VA 22313

Dear Sir:

Submitted herewith is one sheet (FIGS. 1 and 2) of replacement sheet of drawings for filing in the above-identified patent application. The replacement sheet of drawings is submitted in response to the Office Action dated March 19, 2003 in which the Examiner indicated approval of the proposed drawing corrections filed on January 3, 2003. As a result, FIGS. 1 and 2 are now descriptively labeled as "Prior Art." Please substitute the enclosed replacement sheet of drawings (FIGS. 1 and 2) for FIGS. 1 and 2 filed on December 6, 2001. Favorable consideration of the enclosed replacement sheet is respectfully requested.

Dated: June 21, 2005

Respectfully submitted,

By 

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